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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10002175	FILING DATE 12/05/2001	CLASS 228	SUBCLASS 180.5	GAU 1725	EXAMINER Edmondson
<b>**APPLICANTS:</b> Bowen Neal;					
<b>**CONTINUING DATA VERIFIED:</b> <i>None - LRG</i>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> <i>None - LRG</i>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO			
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		M4065.0493/P493			
Verified and Acknowledged Examiners's initials					
TITLE : Stacked chip connection using stand off stitch bonding					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Assistant Examiner	Total Claims <input type="checkbox"/> Print Claim for O.G. <input type="checkbox"/>
<b>ISSUE FEE</b>		Primary Examiner	<b>DRAWING</b>
Amount Due	Date Paid		Sheets Drwg. <input type="checkbox"/> Figs. Drwg. <input type="checkbox"/> Print Fig. <input type="checkbox"/>
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	Application Examiner
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